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# **PALM INTRANET**

## Application Number Information

Application Number: **09/852372** Assignments Examiner Number: **71302 / MORSE, GREG**  
 Filing Date: **05/10/2001** Group Art Unit: **2134**  
 Effective Date: **05/10/2001** Class/Subclass: **713/193.000**  
 Application Received: **05/11/2001** Lost Case: **NO**  
 Patent Number: Interference Number:  
 Issue Date: **00/00/0000** Unmatched Petition: **NO**  
 Date of Abandonment: **00/00/0000** L&R Code: Secrecy Code: **1**  
 Attorney Docket Number: **2000.038300/TT3756** Third Level Review: **NO** Secrecy Order: **NO**  
 Status: **30 /DOCKETED NEW CASE - READY FOR EXAMINATION** Status Date: **12/14/2001**  
 Confirmation Number: **5417** Oral Hearing: **NO**  
 Title of Invention: **SECURE EXECUTION BOX**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
<b>09852372</b>	<b>21C9</b>	<b>11/26/2003</b>	<b>No Charge to Location</b>	<b>No Charge to Name</b>	<b>NORFLEET,CASSIUS</b>	<b>FRAN/01</b>

Appln Info Contents Petition Info Atty/Agent Info Continuity Data Foreign Data Inventors A

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US 20030028781A1

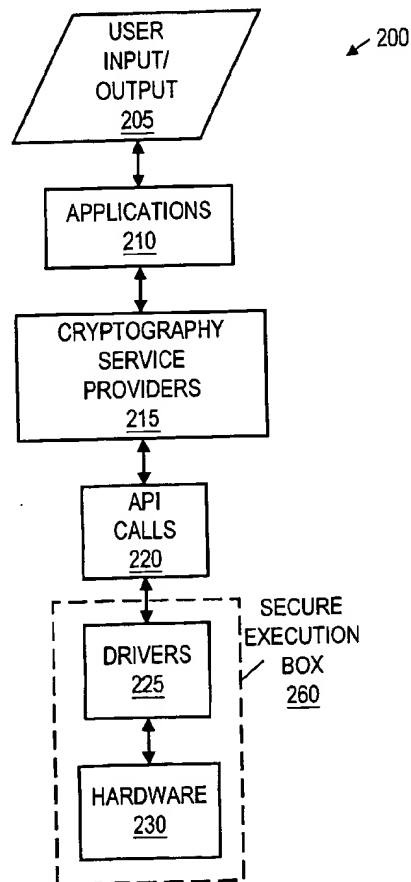
(19) **United States**(12) **Patent Application Publication** (10) Pub. No.: **US 2003/0028781 A1**  
Strongin (43) Pub. Date: **Feb. 6, 2003**(54) **MECHANISM FOR CLOSING BACK DOOR  
ACCESS MECHANISMS IN PERSONAL  
COMPUTER SYSTEMS****Publication Classification**(51) Int. Cl.<sup>7</sup> ..... **H04K 1/00**  
(52) U.S. Cl. .... **713/182**(76) Inventor: **Geoffrey S. Strongin, Austin, TX (US)**(57) **ABSTRACT**

Correspondence Address:  
**Louis H. Iselin, Ph.D.**  
**Williams, Morgan & Amerson, P.C.**  
**7676 Hillmont, Suite 250**  
**Houston, TX 77040 (US)**

Methods, devices, and systems for closing back door access mechanisms. A processor includes a first register configured to store one or more hardware-debug-test (HDT) enable bits, a first control logic coupled to receive a plurality of HDT input signals, and a second control logic coupled to the first register. The first control logic is coupled to access the first register. The second control logic is configured to store one or more default values in the first register in response to a reset of the processor. Another processor includes a first control logic coupled to receive a plurality of microcode inputs, a first register coupled to the first control logic, and a second control logic coupled to the first register. The first register is configured to store one or more microcode loader enable bits. The second control logic is configured to store one or more default values in the first register in response to a reset of the processor.

(21) Appl. No.: **09/853,226**(22) Filed: **May 11, 2001****Related U.S. Application Data**

(63) Continuation-in-part of application No. 09/852,372, filed on May 10, 2001. Continuation-in-part of application No. 09/852,942, filed on May 10, 2001.





US 20030041248A1

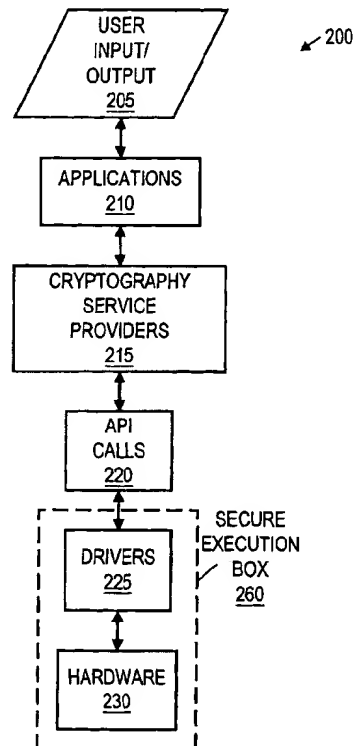
(19) **United States**(12) **Patent Application Publication**  
**Weber et al.**(10) **Pub. No.: US 2003/0041248 A1**(43) **Pub. Date: Feb. 27, 2003**(54) **EXTERNAL LOCKING MECHANISM FOR  
PERSONAL COMPUTER MEMORY  
LOCATIONS**(76) **Inventors: Frederick D. Weber, San Jose, CA  
(US); Dale E. Gulick, Austin, TX (US);  
Geoffrey S. Strongin, Austin, TX (US)****Correspondence Address:****Louis H. Iselin, Ph.D.****Williams, Morgan & Amerson, P.C.****Suite 250****7676 Hillmont****Houston, TX 77040 (US)**(21) **Appl. No.: 09/870,889**(22) **Filed: May 30, 2001****Related U.S. Application Data**

(63) Continuation-in-part of application No. 09/853,395, filed on May 11, 2001. Continuation-in-part of application No. 09/853,446, filed on May 11, 2001. Continuation-in-part of application No. 09/853,447, filed on May 11, 2001. Continuation-in-part of application No. 09/853,225, filed on May 11, 2001. Continuation-in-part of application No. 09/853,226, filed on May 11, 2001. Continuation-in-part of application No. 09/854,040, filed on May 11, 2001. Continuation-in-part of application No. 09/853,465, filed on May 11,

2001. Continuation-in-part of application No. 09/853,443, filed on May 11, 2001. Continuation-in-part of application No. 09/853,437, filed on May 11, 2001. Continuation-in-part of application No. 09/853,335, filed on May 11, 2001. Continuation-in-part of application No. 09/853,234, filed on May 11, 2001. Continuation-in-part of application No. 09/852,372, filed on May 10, 2001. Continuation-in-part of application No. 09/852,942, filed on May 10, 2001.

**Publication Classification**(51) **Int. Cl.<sup>7</sup> ..... H04K 1/00**  
(52) **U.S. Cl. .... 713/182**(57) **ABSTRACT**

A method and system for providing an external locking mechanism for memory locations. The memory includes a first plurality of storage locations configured with BIOS data and a second plurality of storage locations. The second plurality of storage locations includes a first plurality of blocks readable only in SMM and a second plurality of blocks readable in SMM and at least one operating mode other than SMM. The computer system includes a bus, a memory coupled to the bus, and a device coupled to access the memory over the bus. The memory includes a plurality of storage locations, divided into a plurality of memory units. The device includes one or more locks configured to control access to one or more of the plurality of memory units.



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## PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = STRONGIN

First Name = GEOFFREY

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60373571</u>	Not Issued	159	04/18/2002	SECURE EXECUTION MODE FUNCTIONAL SPECIFICATION	STRONGIN, GEOFFREY
<u>10654734</u>	Not Issued	020	09/04/2003	COMPUTER SYSTEM EMPLOYING A TRUSTED EXECUTION ENVIRONMENT INCLUDING A MEMORY CONTROLLER CONFIGURED TO CLEAR MEMORY	STRONGIN, GEOFFREY S.
<u>10161500</u>	Not Issued	030	05/31/2002	SECURE EXECUTION MODE EXCEPTIONS	STRONGIN, GEOFFREY S.
<u>10160984</u>	Not Issued	030	05/31/2002	TRUSTED CLIENT UTILIZING SECURITY KERNEL UNDER SECURE EXECUTION MODE	STRONGIN, GEOFFREY S.
<u>10157503</u>	Not Issued	041	05/29/2002	METHOD AND APPARATUS FOR STORING AND RETRIEVING SECURITY ATTRIBUTES	STRONGIN, GEOFFREY S.
<u>10157465</u>	Not Issued	030	05/29/2002	METHOD AND APPARATUS FOR STORING AND RETRIEVING SECURITY ATTRIBUTES	STRONGIN, GEOFFREY S.
<u>10157309</u>	Not Issued	030	05/29/2002	METHOD AND APPARATUS FOR STORING AND RETRIEVING SECURITY ATTRIBUTES	STRONGIN, GEOFFREY S.
<u>10108253</u>	Not Issued	030	03/27/2002	SYSTEM AND METHOD PROVIDING REGION-GRANULAR, HARDWARE-CONTROLLED MEMORY ENCRYPTION	STRONGIN, GEOFFREY S.
<u>10107784</u>	Not Issued	030	03/27/2002	INPUT/OUTPUT PERMISSION BITMAPS FOR COMPARTMENTALIZED SECURITY	STRONGIN, GEOFFREY S.
<u>10107776</u>	Not Issued	030	03/27/2002	SYSTEM AND METHOD FOR CONTROLLING DEVICE-TO-DEVICE ACCESSSES WITHIN A COMPUTER SYSTEM	STRONGIN, GEOFFREY S.
<u>10107633</u>	Not Issued	030	03/27/2002	METHOD AND APPARATUS FOR IMPROVED SECURITY IN A DATA PROCESSOR	STRONGIN, GEOFFREY S.
<u>10082766</u>	<u>6564272</u>	150	02/25/2002	NON-COHERENT CACHE BUFFER FOR READ ACCESSSES TO SYSTEM	STRONGIN, GEOFFREY S.S.

				MEMORY	
<u>10051448</u>	Not Issued	071	01/18/2002	METHOD AND APPARATUS FOR PROTECTING PAGE TRANSLATIONS	STRONGIN, GEOFFREY S.
<u>10047188</u>	Not Issued	030	01/15/2002	METHOD AND APPARATUS FOR MULTI-TABLE ACCESSING OF INPUT/OUTPUT DEVICES USING TARGET SECURITY	STRONGIN, GEOFFREY S.
<u>10044667</u>	Not Issued	030	01/11/2002	METHOD AND APPARATUS FOR LINEAR ADDRESS BASED PAGE LEVEL SECURITY SCHEME TO DETERMINE CURRENT SECURITY CONTEXT	STRONGIN, GEOFFREY S.
<u>10011151</u>	Not Issued	030	12/05/2001	SYSTEM AND METHOD FOR HANDLING DEVICE ACCESSES TO A MEMORY PROVIDING INCREASED MEMORY ACCESS SECURITY	STRONGIN, GEOFFREY S.
<u>10010569</u>	Not Issued	041	11/13/2001	MEMORY MANAGEMENT SYSTEM AND METHOD FOR PROVIDING PHYSICAL ADDRESS BASED MEMORY ACCESS SECURITY	STRONGIN, GEOFFREY S.
<u>10010161</u>	Not Issued	030	11/13/2001	MEMORY MANAGEMENT SYSTEM AND METHOD PROVIDING LINEAR ADDRESS BASED MEMORY ACCESS SECURITY	STRONGIN, GEOFFREY S.
<u>10005271</u>	Not Issued	071	12/05/2001	MEMORY MANAGEMENT SYSTEM AND METHOD PROVIDING INCREASED MEMORY ACCESS SECURITY	STRONGIN, GEOFFREY S.
<u>10005248</u>	Not Issued	030	12/03/2001	METHOD AND APPARATUS FOR RESTRICTED EXECUTION OF SECURITY SENSITIVE INSTRUCTIONS	STRONGIN, GEOFFREY S.
<u>10005225</u>	Not Issued	030	12/03/2001	METHOD AND APPARATUS FOR REDEFINING ARCHITECTURE TABLES TO PROVIDE MULTI-LEVEL COMPARTMENTALIZED SECURITY	STRONGIN, GEOFFREY S.
<u>09999881</u>	Not Issued	030	10/31/2001	METHOD AND APPARATUS FOR PHYSICAL ADDRESS-BASED SECURITY TO DETERMINE TARGET SECURITY	STRONGIN, GEOFFREY S.
<u>09901547</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM FOR COMMUNICATING DATA USING SEPARATE CHANNELS FOR DATA AND CONTROL CODES	STRONGIN, GEOFFREY S.
<u>09901531</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM WITH HIDDEN AUTHENTICATION COMMANDS	STRONGIN, GEOFFREY S.
<u>09901520</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM WITH PRIVILEGED MODE DECRYPTION OF CONTROL CODES	STRONGIN, GEOFFREY S.

<u>09901503</u>	Not Issued	071	07/09/2001	COMPUTER SYSTEM WITH PRIVILEGED-MODE MODEM DRIVER	STRONGIN, GEOFFREY S.
<u>09901421</u>	Not Issued	030	07/09/2001	METHOD AND APPARATUS FOR PREVENTING RADIO COMMUNICATION SYSTEM ACCESS BY AN UNAUTHORIZED MODEM	STRONGIN, GEOFFREY S.
<u>09901329</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM FOR COMMUNICATING DATA USING ENCRYPTED DATA AND UNENCRYPTED CONTROL CODES	STRONGIN, GEOFFREY S.
<u>09901328</u>	Not Issued	030	07/09/2001	METHOD AND APPARATUS FOR DETECTING THE UNAUTHORIZED USE OF A RADIO COMMUNICATION SYSTEM BY A MODEM	STRONGIN, GEOFFREY S.
<u>09901212</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM WITH PRIVILEGED MODE DRIVER AUTHENTICATION	STRONGIN, GEOFFREY S.
<u>09901176</u>	Not Issued	030	07/09/2001	PERIPHERAL DEVICE WITH SECURE DRIVER	STRONGIN, GEOFFREY S.
<u>09901169</u>	Not Issued	030	07/09/2001	METHOD AND APPARATUS FOR DETECTING AN UNAUTHORIZED SOFTWARE-BASED MODEM USING A SOFTWARE SIGNATURE	STRONGIN, GEOFFREY S.
<u>09901158</u>	Not Issued	030	07/09/2001	SOFTWARE MODEM WITH PRIVILEGED MODE OVERSIGHT OF CONTROL PARAMETERS	STRONGIN, GEOFFREY S.
<u>09870890</u>	Not Issued	030	05/30/2001	SECURE BOOTING OF A PERSONAL COMPUTER SYSTEM	STRONGIN, GEOFFREY S.
<u>09870889</u>	Not Issued	030	05/30/2001	EXTERNAL LOCKING MECHANISM FOR PERSONAL COMPUTER MEMORY LOCATIONS	STRONGIN, GEOFFREY S.
<u>09853465</u>	Not Issued	030	05/11/2001	CRYPTOGRAPHIC COMMAND-RESPONSE ACCESS TO A MEMORY IN A PERSONAL COMPUTER SYSTEM	STRONGIN, GEOFFREY S.
<u>09853447</u> <i>mg</i>	Not Issued	030	05/11/2001	INTEGRATED CIRCUIT FOR SECURITY AND MANAGEABILITY	STRONGIN, GEOFFREY S.
<u>09853443</u> <i>mg</i>	Not Issued	030	05/11/2001	PROTECTION MECHANISM FOR BIOMETRIC INPUT DATA	STRONGIN, GEOFFREY S.
<u>09853437</u> <i>mg</i>	Not Issued	030	05/11/2001	PERSONAL COMPUTER SECURITY MECHANISM	STRONGIN, GEOFFREY S.
<u>09853395</u> <i>mg</i>	Not Issued	030	05/11/2001	ENHANCED SECURITY AND MANAGEABILITY USING SECURE STORAGE IN A PERSONAL COMPUTER SYSTEM	STRONGIN, GEOFFREY S.
<u>09853335</u> <i>mg</i>	Not Issued	030	05/11/2001	ASSET SHARING BETWEEN HOST PROCESSOR AND SECURITY	STRONGIN, GEOFFREY S.



				HARDWARE	
<u>09853234</u>	Not Issued	041	05/11/2001	INTERRUPTABLE AND RE-ENTERABLE SYSTEM MANAGEMENT MODE PROGRAMMING CODE	STRONGIN, GEOFFREY S.
<u>09853226</u> <i>mg</i>	Not Issued	030	05/11/2001	MECHANISM FOR CLOSING BACK DOOR ACCESS MECHANISMS IN PERSONAL COMPUTER SYSTEMS	STRONGIN, GEOFFREY S.
<u>09853225</u> <i>mg</i>	Not Issued	030	05/11/2001	SECURE SYSTEM MODE DURATION MANAGEMENT	STRONGIN, GEOFFREY A.
<u>09852372</u> <i>mg</i>	Not Issued	030	05/10/2001	SECURE EXECUTION BOX	STRONGIN, GEOFFREY S.
<u>09825908</u> <i>mg</i>	Not Issued	030	04/04/2001	METHOD AND APPARATUS FOR SECURITY	STRONGIN, GEOFFREY S.
<u>09825905</u>	Not Issued	030	04/04/2001	METHOD AND APPARATUS FOR SECURING PORTIONS OF MEMORY	STRONGIN, GEOFFREY S.
<u>09702147</u>	<u>6385705</u>	150	10/30/2000	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES IN A MULTIPROCESSOR SYSTEM	STRONGIN, GEOFFREY
<u>09626604</u>	<u>6654860</u>	150	07/27/2000	METHOD AND APPARATUS FOR REMOVING SPECULATIVE MEMORY ACCESSES FROM A MEMORY ACCESS QUEUE FOR ISSUANCE TO MEMORY OR DISCARDING	STRONGIN, GEOFFREY S.S.
<u>09569039</u>	<u>6381672</u>	150	05/11/2000	SPECULATIVE OPENING OF A NEW PAGE WHEN APPROACHING PAGE BOUNDARY DURING READ/WRITE OF ISOCHRONOUS STREAMS	STRONGIN, GEOFFREY S.S.
<u>09564765</u>	Not Issued	041	05/04/2000	MEMORY CONTROLLER PROVIDING MULTIPLE POWER MODES FOR ACCESSING MEMORY DEVICES BY REORDERING MEMORY TRANSACTIONS	STRONGIN, GEOFFREY S. S.

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## Inventor Name Search Result

Your Search was:

Last Name = GULICK

First Name = DALE

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60373571</u> ✓	Not Issued	159	04/18/2002	SECURE EXECUTION MODE FUNCTIONAL SPECIFICATION	GULICK, DALE E.
<u>60306361</u>	Not Issued	159	07/18/2001	MESSAGE BASED POWER MANAGEMENT	GULICK, DALE E.
<u>10718166</u>	Not Issued	018	11/20/2003	SOFT MAGNETIC COMPOUND FOR INJECTION MOLDED RETURN PATHS	GULICK, DALE
<u>10654734</u>	Not Issued	020	09/04/2003	COMPUTER SYSTEM EMPLOYING A TRUSTED EXECUTION ENVIRONMENT INCLUDING A MEMORY CONTROLLER CONFIGURED TO CLEAR MEMORY	GULICK, DALE E.
<u>10647397</u>	Not Issued	030	08/25/2003	CONFIGURING A COMMUNICATION LINK INTERFACE	GULICK, DALE E.
<u>10185195</u>	Not Issued	030	06/28/2002	MESSAGE BASED POWER MANAGEMENT	GULICK, DALE E.
<u>10185171</u>	Not Issued	030	06/28/2002	MESSAGE BASED POWER MANAGEMENT IN A MULTI-PROCESSOR SYSTEM	GULICK, DALE E.
<u>10066879</u>	Not Issued	030	02/04/2002	ASF STATE DETERMINATION USING CHIPSET-RESIDENT WATCHDOG TIMER	GULICK, DALE E.
<u>10062390</u>	Not Issued	030	02/01/2002	SERIAL PIN REFLECTION OF GPIO TERMINALS	GULICK, DALE E.
<u>10034967</u>	Not Issued	071	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE AND AN INTEGRATED I/O HUB	GULICK, DALE E.
<u>10034878</u>	<u>6697890</u>	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED I/O INTERFACE	GULICK, DALE E.
<u>10034560</u>	Not Issued	061	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE	GULICK, DALE E.
<u>10033142</u> ✓	Not Issued	030	11/01/2001	MICROCOMPUTER BRIDGE ARCHITECTURE WITH AN EMBEDDED MICROCONTROLLER	GULICK, DALE E.

<u>10023233</u>	Not Issued	030	03/20/2002	MANAGING A CONTROLLER EMBEDDED IN A BRIDGE	GULICK, DALE E.
<u>10005648</u>	Not Issued	030	12/03/2001	EMBEDDED PROCESSOR SUPPORTING BOTH ACPI AND ASF OPERATIONS	GULICK, DALE E.
<u>09998758</u>	Not Issued	030	11/30/2001	SWITCHING I/O NODE FOR CONNECTION IN A MULTIPROCESSOR COMPUTER SYSTEM	GULICK, DALE E.
<u>09933290</u>	6625743	150	08/20/2001	A METHOD FOR SYNCHRONIZING GENERATION AND CONSUMPTION OF ISOCHRONOUS DATA	GULICK, DALE E.
<u>09904374</u>	6671748	150	07/11/2001	METHOD AND APPARATUS FOR PASSING DEVICE CONFIGURATION INFORMATION TO A SHARED CONTROLLER	GULICK, DALE E.
<u>09904373</u>	Not Issued	041	07/11/2001	METHOD AND APPARATUS FOR PROVIDING CAPABILITY INFORMATION TO A SHARED CONTROLLER	GULICK, DALE E.
<u>09871511</u>	Not Issued	030	05/30/2001	MONOTONIC COUNTER MECHANISM FOR COMPUTER SYSTEM SECURITY	GULICK, DALE E.
<u>09871084</u>	Not Issued	030	05/30/2001	LOCKING MECHANISM OVERRIDE AND DISABLE FOR PERSONAL COMPUTER ROM ACCESS PROTECTION	GULICK, DALE E.
<u>09870890</u>	Not Issued	030	05/30/2001	SECURE BOOTING OF A PERSONAL COMPUTER SYSTEM	GULICK, DALE E.
<u>09870889</u> <i>mb</i>	Not Issued	030	05/30/2001	EXTERNAL LOCKING MECHANISM FOR PERSONAL COMPUTER MEMORY LOCATIONS	GULICK, DALE E.
<u>09854040</u> <i>mb</i>	Not Issued	030	05/11/2001	CRYPTOGRAPHIC RANDOMNESS REGISTER FOR COMPUTER SYSTEM SECURITY	GULICK, DALE E.
<u>09853447</u> <i>mb</i>	Not Issued	030	05/11/2001	INTEGRATED CIRCUIT FOR SECURITY AND MANAGEABILITY	GULICK, DALE E.
<u>09853446</u>	Not Issued	030	05/11/2001	RESOURCE SEQUESTER MECHANISM	GULICK, DALE E.
<u>09853443</u>	Not Issued	030	05/11/2001	PROTECTION MECHANISM FOR BIOMETRIC INPUT DATA	GULICK, DALE E.
<u>09853437</u> <i>mb</i>	Not Issued	030	05/11/2001	PERSONAL COMPUTER SECURITY MECHANISM	GULICK, DALE E.
<u>09853395</u> <i>mb</i>	Not Issued	030	05/11/2001	ENHANCED SECURITY AND MANAGEABILITY USING SECURE STORAGE IN A PERSONAL COMPUTER SYSTEM	GULICK, DALE E.
<u>09853335</u> <i>mb</i>	Not Issued	030	05/11/2001	ASSET SHARING BETWEEN HOST PROCESSOR AND SECURITY	GULICK, DALE E.

				HARDWARE	
<u>09853234</u>	Not Issued	041	05/11/2001	INTERRUPTABLE AND RE-ENTERABLE SYSTEM MANAGEMENT MODE PROGRAMMING CODE	GULICK, DALE E.
<u>09853225</u>	Not Issued	030	05/11/2001	SECURE SYSTEM MODE DURATION MANAGEMENT	GULICK, DALE E.
<u>09852942</u>	Not Issued	030	05/10/2001	COMPUTER SYSTEM ARCHITECTURE FOR ENHANCED SECURITY AND MANAGEABILITY	GULICK, DALE E.
<u>09852372</u>	Not Issued	030	05/10/2001	SECURE EXECUTION BOX	GULICK, DALE E.
<u>09832374</u>	Not Issued	161	04/11/2001	SOFT MAGNETIC COMPOUND FOR INJECTION MOLDED RETURN PATHS	GULICK, DALE
<u>09772423</u>	6473663	150	01/29/2001	NOISE ELIMINATION IN A USB CODEC	GULICK, DALE E.
<u>09747016</u>	Not Issued	167	12/20/2000	MONOLITHIC PC AUDIO CIRCUIT	GULICK, DALE E.
<u>09715946</u>	Not Issued	061	11/17/2000	SYSTEM AND METHOD FOR ENABLING ENUMERATION OF PHYSICALLY PARTITIONED DEVICES	GULICK, DALE E.
<u>09702147</u>	6385705	150	10/30/2000	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES IN A MULTIPROCESSOR SYSTEM	GULICK, DALE E.
<u>09652647</u>	Not Issued	041	08/31/2000	SYSTEM AND METHOD FOR MONITORING AND CONTROLLING A POWER-MANAGEABLE RESOURCE	GULICK, DALE E.
<u>09636417</u>	Not Issued	161	08/10/2000	HIGH SPEED DATA BUS INCORPORATING SERIES RESISTOR COUPLED BUS DEVICES	GULICK, DALE E.
<u>09577527</u>	Not Issued	061	05/24/2000	GRAPHICS SUBSYSTEM INCLUDING A RAMDAC IC WITH DIGITAL VIDEO STORAGE INTERFACE FOR CONNECTION TO A GRAPHICS BUS	GULICK, DALE E.
<u>09577526</u>	Not Issued	061	05/24/2000	GRAPHICS SUBSYSTEM INCLUDING A RAMDAC IC INCLUDING INTEGRATED VIDEO OVERLAY CIRCUITRY FOR CONNECTION TO A GRAPHICS BUS	GULICK, DALE E.
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